TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WZ14FU,TC7WZ14FK

Triple Schmitt Inverter

Features

High output drive : ±24 mA (min) at V_{CC} = 3 V

Super high speed operation: tpd = 3.7 ns (typ.)

at $V_{CC} = 5 \text{ V}, 50 \text{ pF}$

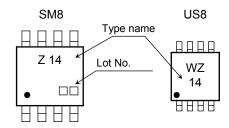
• Operation voltage range : V_{CC (opr)} = 1.65~5.5 V

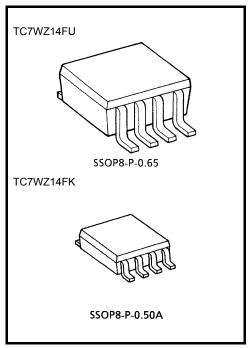
• 5.5-V tolerant inputs

• 5.5-V power down protection outputs

 Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

Marking





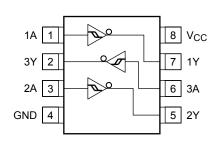
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	Vout	-0.5~6	V
Input diode current	IIK	-20	mA
Output diode current	lok	-20	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	TL	260	°C

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Logic Diagram



Truth Table

INPUT	OUTPUT				
Α	Y				
L	Н				
Н	L				

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Voo	1.65~5.5	V
	V _{CC}	1.5~5.5 (Note 1)	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	\/a=	0~5.5 (Note 2)	V
	Vout	0~ V _{CC} (Note 3)	
Operating temperature	T _{opr}	−40~85	°C

Note 1 : Data retention only

Note 2 : $V_{CC} = 0 V$

Note 3 : High or low state



Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition V _{CC} (V)		Ta = 25°C			Ta = -40~85°C		Unit		
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
High-level				1.65	0.6	1.0	1.4	0.6	1.4		
					0.7	1.1	1.5	0.7	1.5		
	V _P	_		2.3	1.0	1.4	1.8	1.0	1.8		
				3.0	1.3	1.75	2.2	1.3	2.2		
				4.5	1.9	2.45	3.1	1.9	3.1		
Threshold					5.5	2.2	2.9	3.6	2.2	3.6	
voltage					1.65	0.2	0.5	0.8	0.2	0.8	
ı					1.8	0.25	0.55	0.9	0.25	0.9	
		.,			2.3	0.4	0.75	1.15	0.4	1.15	V
	Low-level	V _N		_	3.0	0.6	1.0	1.5	0.6	1.5	V
						1.0	1.43	2.0	1.0	2.0	
					5.5	1.2	1.7	2.4	1.2	2.4	
	1				1.65	0.1	0.48	0.9	0.1	0.9	
					1.8	0.15	0.54	1.0	0.15	1.0	-
Llyatarasia	oltogo	V.				0.25	0.65	1.1	0.25	1.1	
Hysteresis vo	ollage	V _H		_	3.0	0.4	0.77	1.2	0.4	1.2	
					0.6	1.01	1.5	0.6	1.5		
					5.5	0.7	1.18	1.7	0.7	1.7	
					1.65	1.55	1.65	_	1.55	_	-
				100 4	2.3	2.2	2.3	_	2.2	_	
			$I_{OH} = -100 \mu A$	3.0	2.9	3.0	_	2.9	_		
					4.5	4.4	4.5	_	4.4	_	
High-level ou	utput voltage	V _{ОН}	$V_{IN} = V_{IL} \\$	I _{OH} = -4 mA	1.65	1.29	1.52		1.29	_	
				I _{OH} = -8 mA	2.3	1.9	2.14		1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.75		2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.62		2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.13		3.8	_	V	
					1.65	_	0	0.1	—	0.1	V
			I _{OH} = 100 μA	2.3		0	0.1	_	0.1		
				10Η – 100 μΑ	3.0	_	0	0.1	_	0.1	
Low-level output voltage				4.5	_	0	0.1	_	0.1	-	
	V _{OL}	$V_{IN} = V_{IH}$	I _{OH} = 4 mA	1.65	_	0.08	0.24	_	0.24		
			I _{OH} = 8 mA	2.3	_	0.1	0.3	_	0.3		
			I _{OH} = 6 mA	3.0	_	0.16	0.4	_	0.4		
			I _{OH} = 24 mA	3.0		0.24	0.55	_	0.55		
			I _{OH} = 32 mA	4.5		0.25	0.55	_	0.55		
Input leakage	e current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5		_	±1	_	±10	μА
Power off lea	akage current	loff	V _{IN} or V _{OU}	_T = 5.5 V	0.0			1	_	10	μΑ
Quiescent su	ipply current	Icc	V _{IN} = 5.5 V or GND		1.65~5.5		_	1	_	10	μΑ

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AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	^t pLH ^t pHL	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	1.8 ± 0.15	2.0	9.1	15.0	2.0	15.6	- ns
			2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5	
			3.3 ± 0.3	1.0	3.7	6.3	1.0	6.5	
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	
		$C_L = 50$ pF, $R_L = 500 \Omega$	3.3 ± 0.3	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.8	3.7	5.9	0.8	6.2	
Input capacitance	C _{IN}	_	0~5.5	_	3.0		_	_	pF
Power dissipation capacitance		(Note 4)	3.3	_	33		_	_	pF
	C _{PD}		5.5	_	43	_	_	_	

Note 4: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

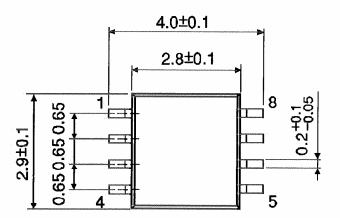
$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$$

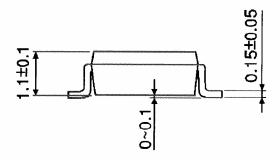
Unit: mm



Package Dimensions

SSOP8-P-0.65



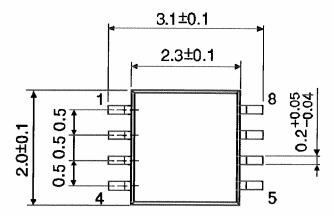


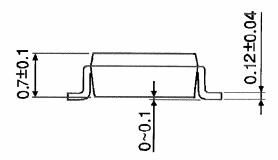
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Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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